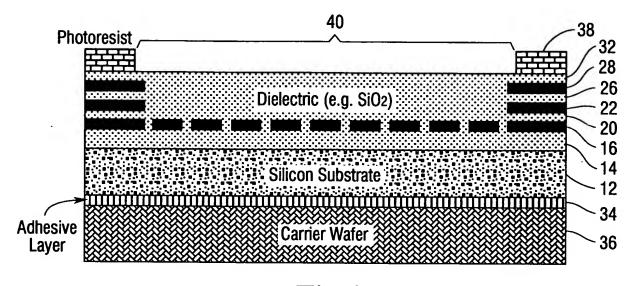


Fig.3





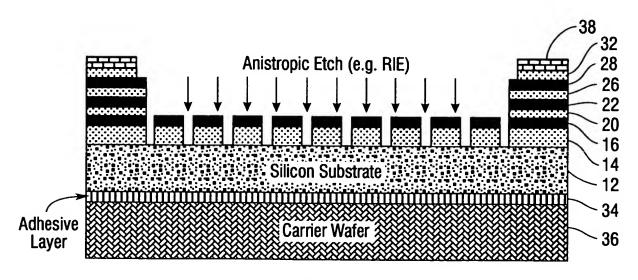


Fig.5

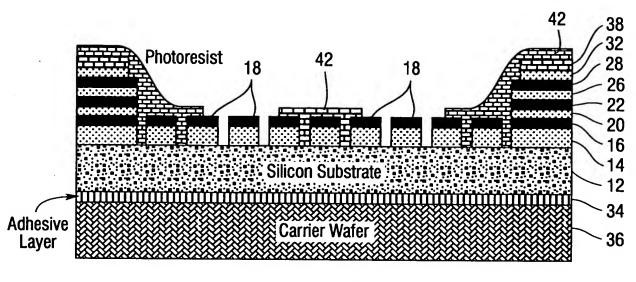


Fig.6

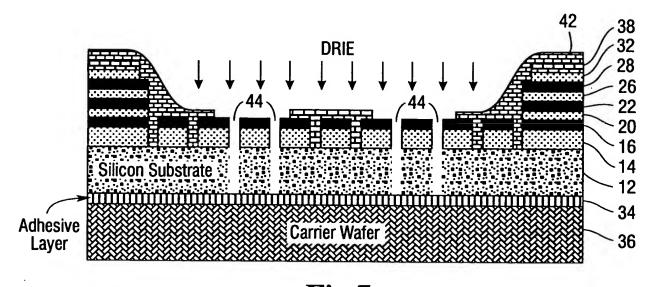


Fig.7

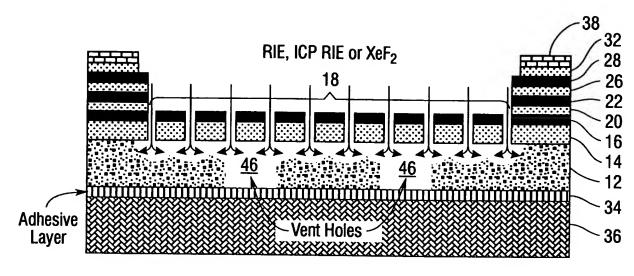
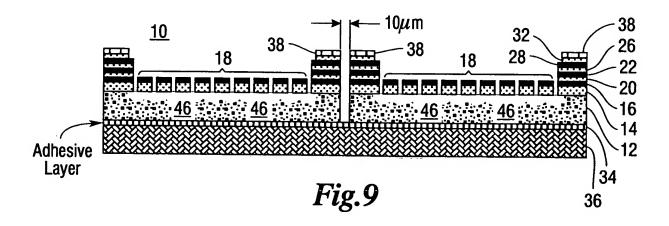
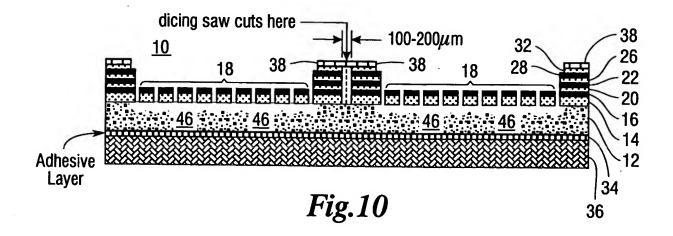


Fig.8





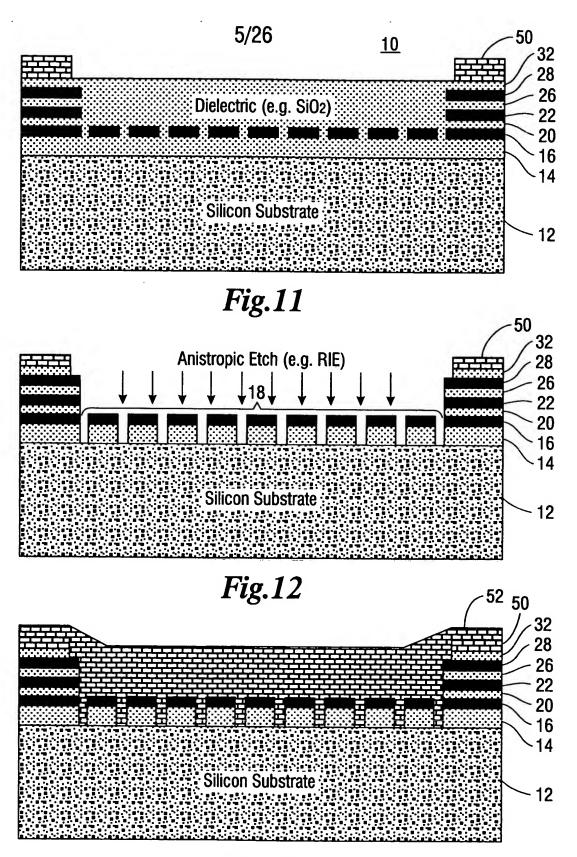


Fig.13

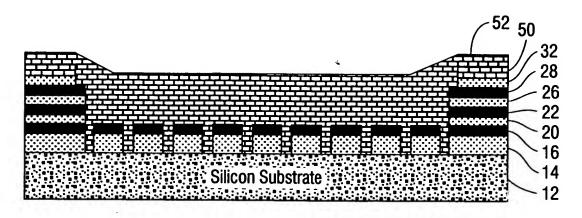


Fig.14

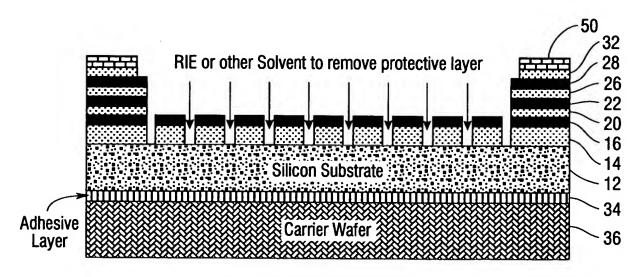


Fig.15

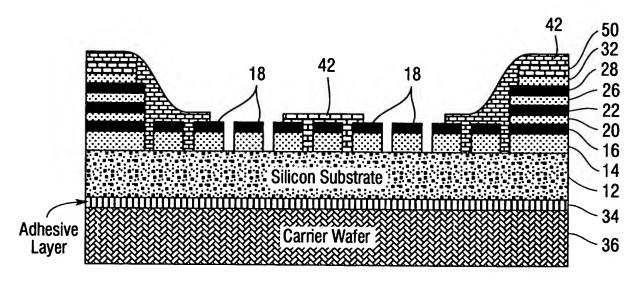


Fig.16

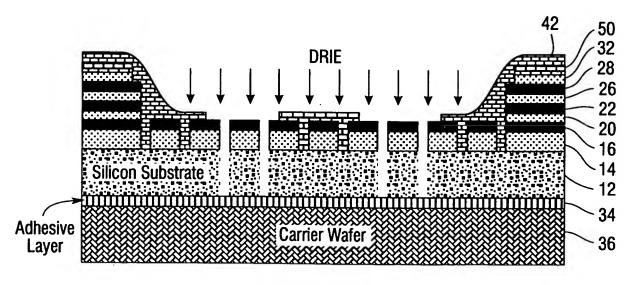


Fig.17

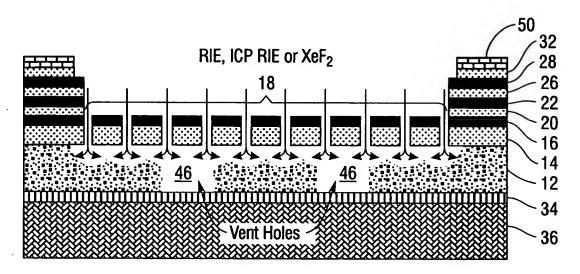


Fig.18

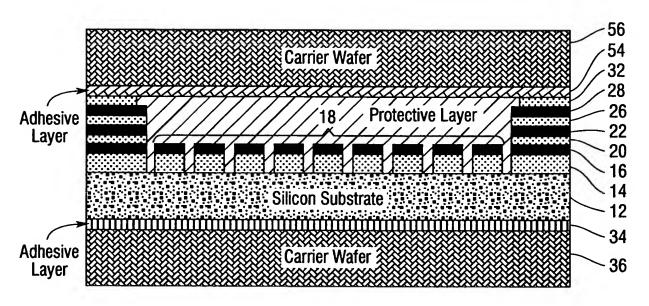
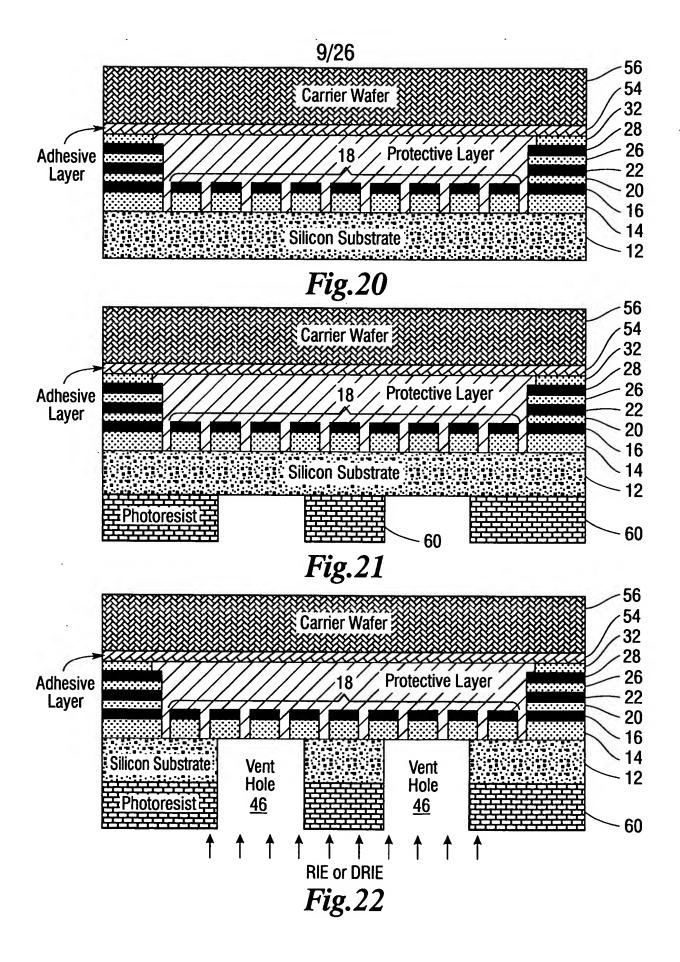
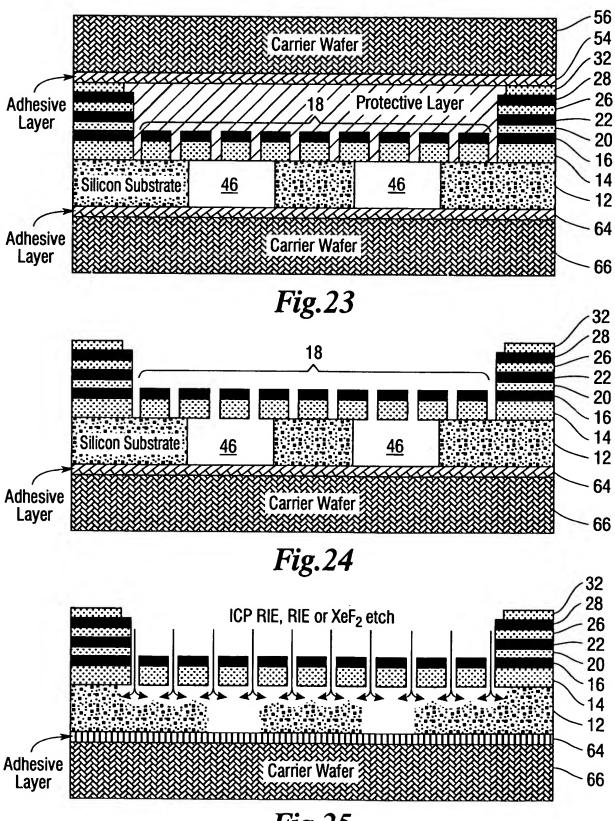


Fig.19





*Fig.25* 

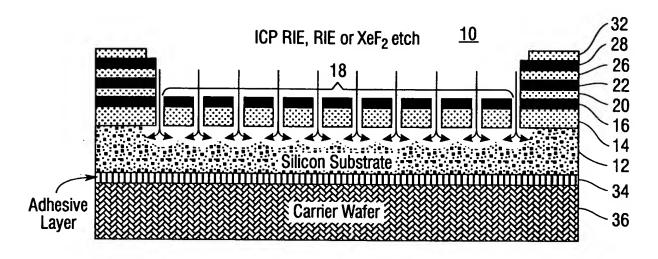


Fig.26

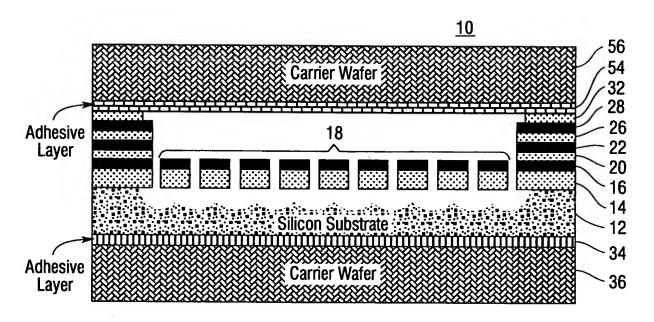


Fig.27

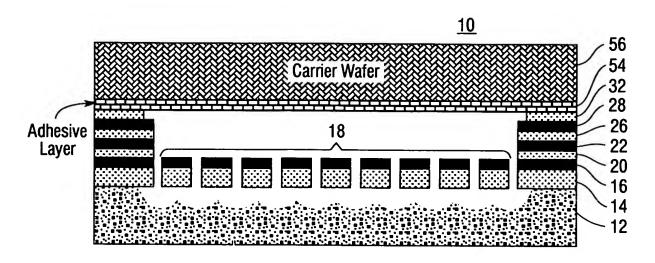


Fig.28

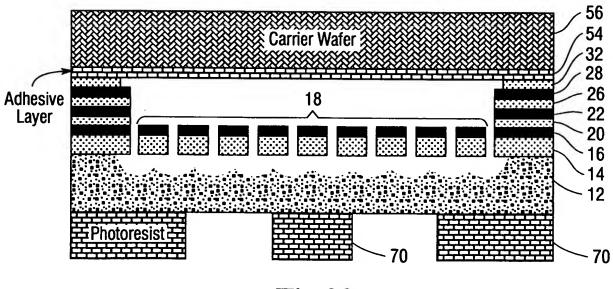
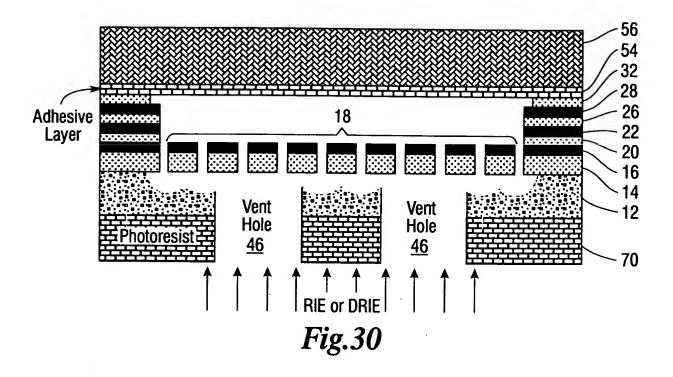


Fig.29



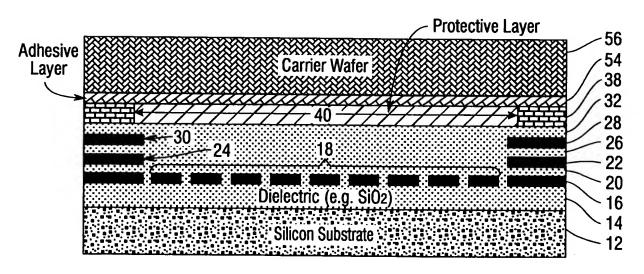
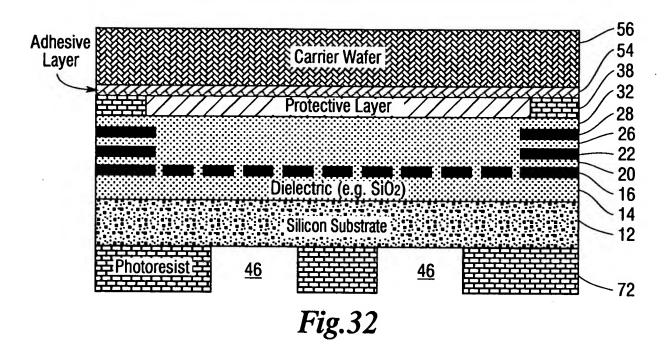
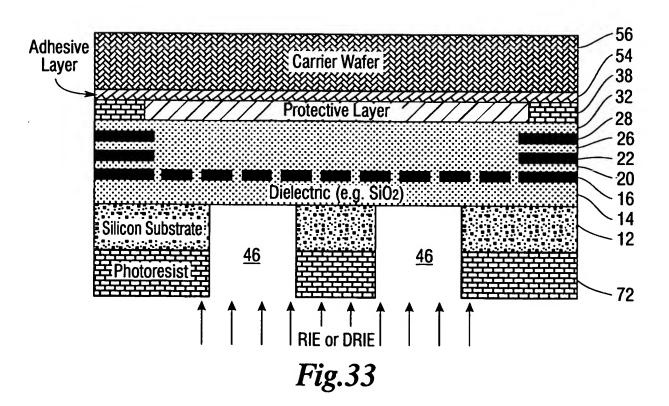
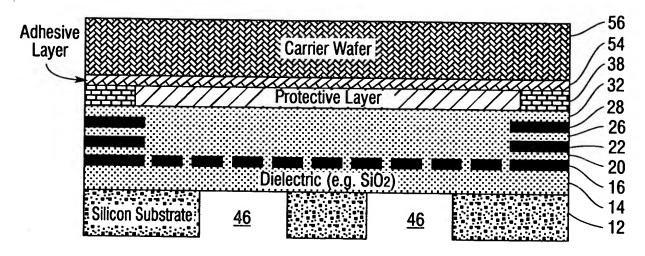


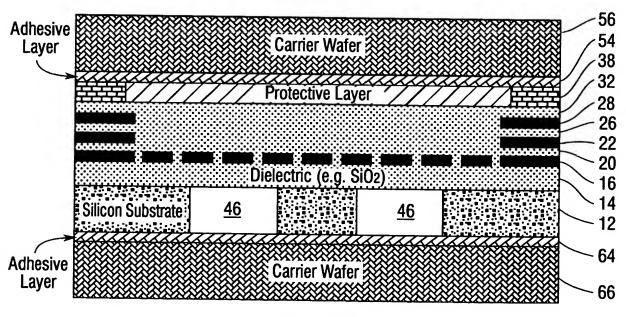
Fig.31



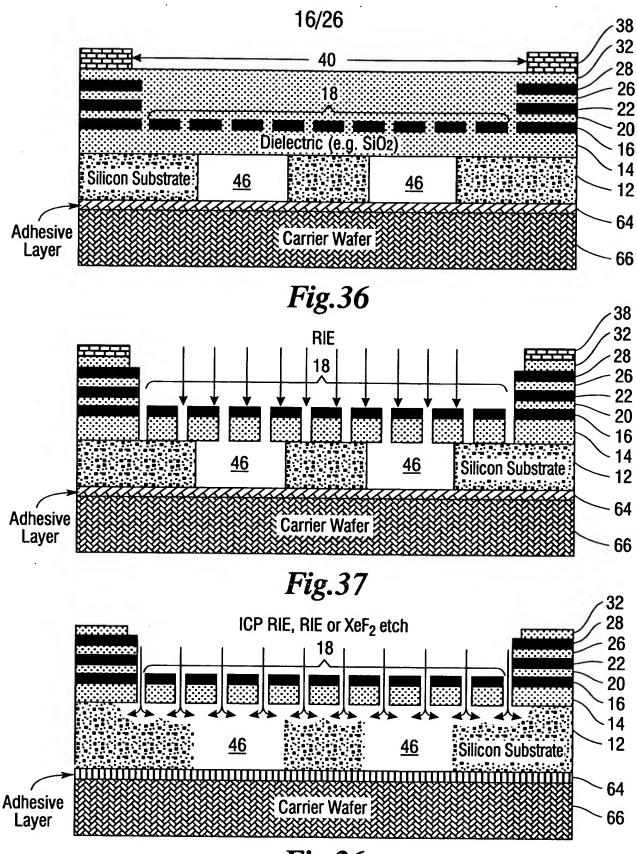




*Fig.34* 



*Fig.35* 



*Fig.26* 

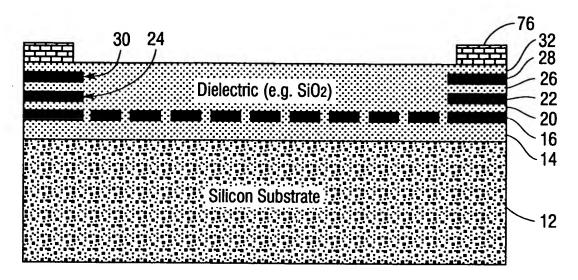


Fig.39

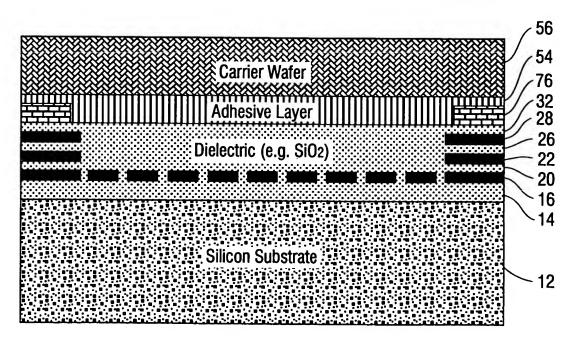


Fig.40

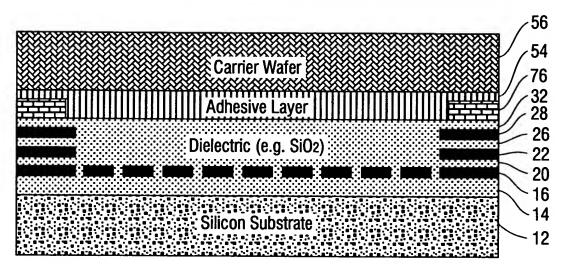
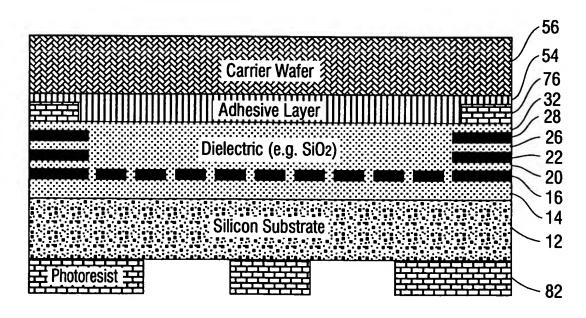
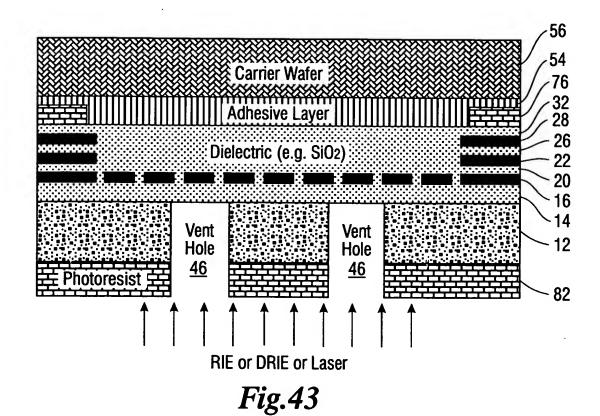
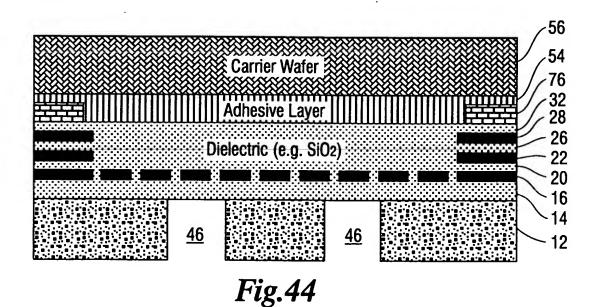


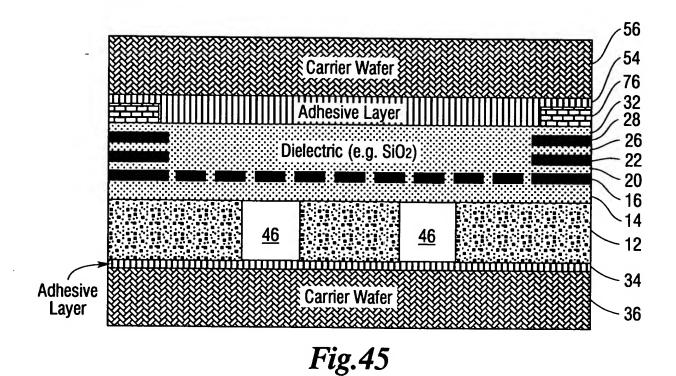
Fig.41

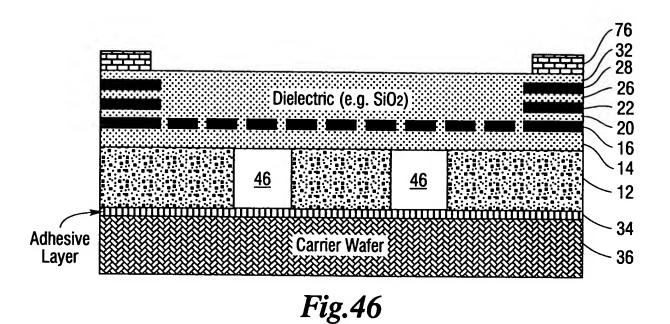


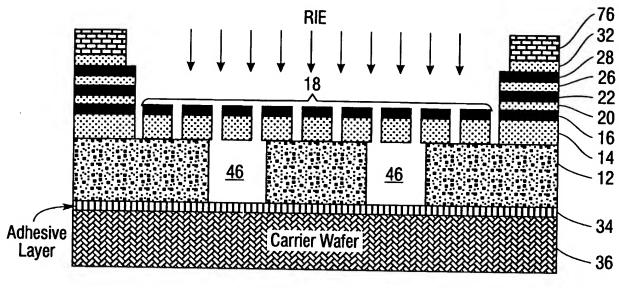
*Fig.42* 











*Fig.47* 

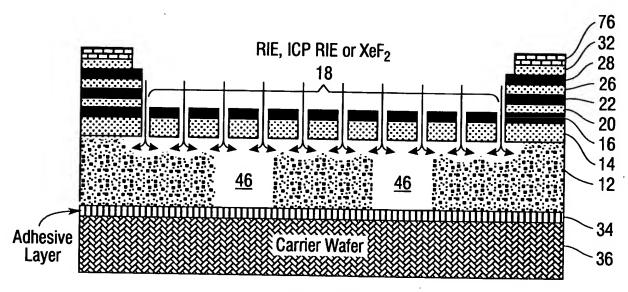


Fig.48

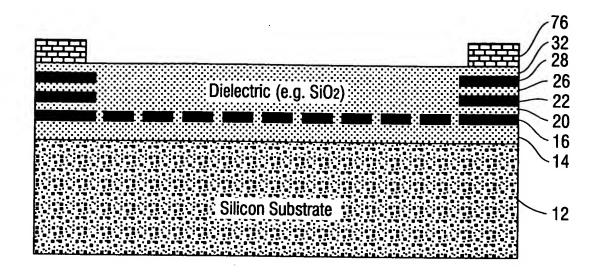


Fig.49

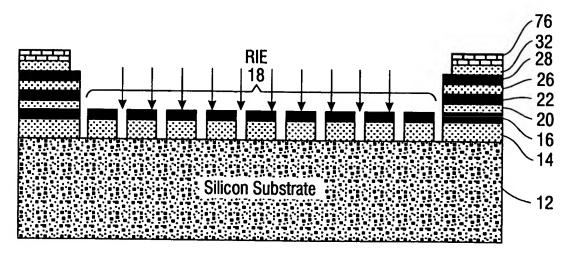


Fig.50

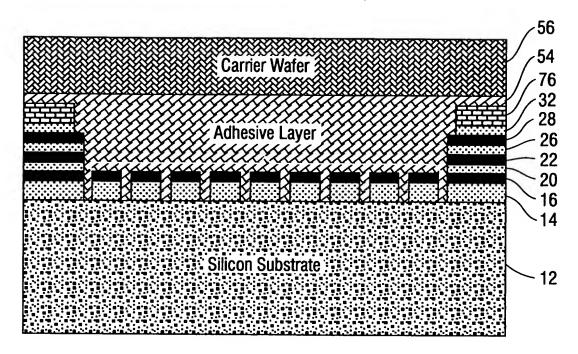


Fig.51

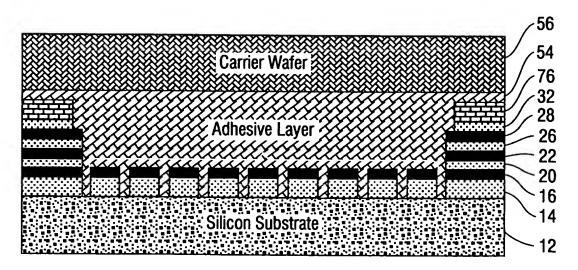
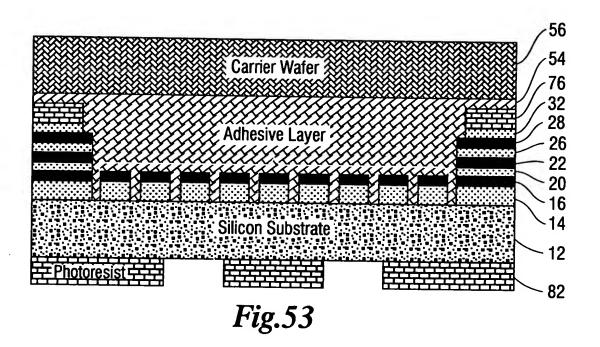
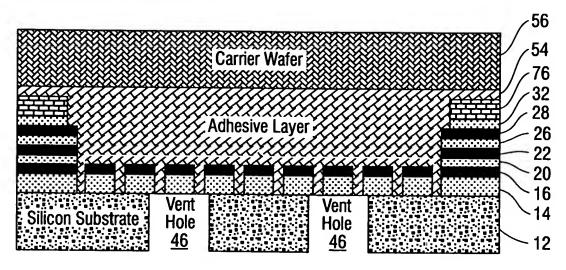


Fig.52



56 54 Carrier Wafer 76 -32 Adhesive Layer 28 26 22 20 16 Silicon Substrate Vent Hole Vent Hole 14 12 <u>46</u> <u>46</u> Photoresist 82 RIE or DRIE or Laser

Fig.54



*Fig.*55

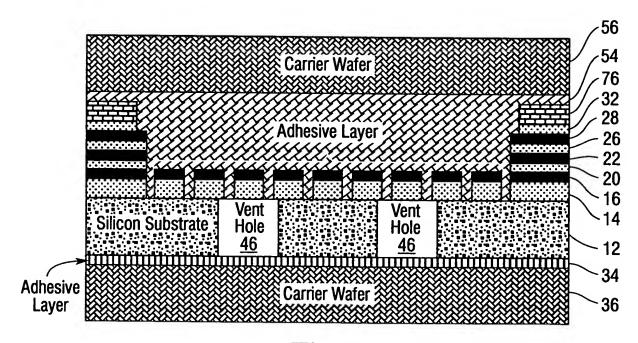
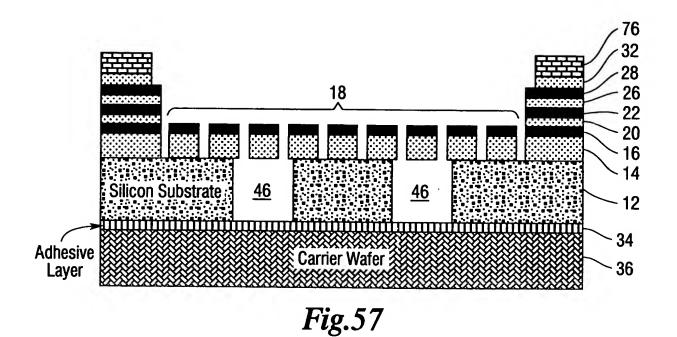


Fig.56



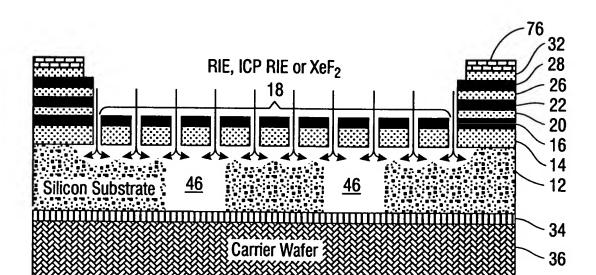


Fig. 58